



Serial No.:10/802,664

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Date of Deposit

Sid Bennett, Reg. No. 53,981

Name of applicant, assignee or
Registered Representative

Signature

Date of Signature

Our Case No. 13050-12

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Shawn D. ROGERS et al.

Serial No. 10/802,664

Filing Date: 03/17/2004

For: CIRCUIT AND METHOD FOR
ENHANCED LOW FREQUENCY
SWITCHING NOISE SUPPRESSION IN
MULTILAYER PRINTED CIRCUIT
BOARDS USING A CHIP CAPACITOR
LATTICE

Examiner: Dickey, Thomas L.

Art Unit: 2826

RESPONSE TO RESTRICTION REQUIREMENT AND AMENDMENT

M/S Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action dated April 24, 2006.

The Claims begin on page 2, and the Remarks begin on page 13.

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01 FC:1201 600.00 DA
02 FC:1202 1200.00 DA